

CPU Verification Engineer

The ideal candidate will have a Bachelor's Degree in Electrical Engineering, Computer Engineering, or Computer Science with five to ten years of experience in the design/verification of ASIC/IC devices.

Job Description: The candidate will be responsible for definition, implementation, debug, and maintenance of CPU architecture verification test suites utilizing extensive Assembly and C level code and interfacing into a System Verilog simulation environment. This person must interface with designers and micro-architects for ensuring coverage goals are established and realized. Test design will span multiple levels of system hierarchy running on a custom 64-bit RISC supercomputer.

The following skills and experience are required:

- Direct experience in design or verification of high performance microprocessors along with broad software design skills
- Proficient in designing CPU hardware specific tests in assembly language and in C
- Working knowledge of CPU systems architecture and debugging in SystemVerilog based verification environments
- Proficient with at least one scripting language (Perl/Python)

The following additional skills and experiences would be a plus:

- Experience establish coverage goals, writing test plans, and organizing verification suites
- Familiarity with randomized verification techniques
- Understanding of compilers

Must be a US Person as defined in EAR 15 CFR Part 772 and ITAR 22 CFR Section 120.15, which includes US Citizenship, US Permanent Residence, or a Protected Person under 8 U.S.C. 1324b(a)(3).

For consideration, please send your resume to careers@firstpasseng.com