CPU Modeling Engineer

The ideal candidate will have a Bachelor's Degree in Electrical Engineering, Computer Engineering, or Computer Science with five to ten years of experience in the design/verification of ASIC/IC devices.

Job Description: The candidate will be responsible for developing high level simulation models and verification test suites during the definition and implementation phases of an advanced microprocessor design. This person will work closely with micro-architecture and design teams to implement accurate models and integrate them into the overall development system. The candidate will also be responsible for organizing microprocessor test suites and designing tests in low level assembly language and C.

The following skills and experience are required:

- Developing timing accurate models of complex hardware architectures
- Integrating and co-developing models with digital simulation environments
- Background on architecture, design and verification of high performance microprocessors along with broad software design skills
- Proficient in C and/or C++ and assembly language

The following additional skills and experiences would be a plus:

- Effective at identifying performance-driven issues and using modeling to assist with convergence on architectural trade-offs
- Familiarity with System Verilog and UVM verification architecture.

Must be a US Person as defined in EAR 15 CFR Part 772 and ITAR 22 CFR Section 120.15, which includes US Citizenship, US Permanent Residence, or a Protected Person under 8 U.S.C. 1324b(a)(3).

For consideration, please send your resume to careers@firstpasseng.com